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In re application of:

1

presently aware, and no art which is closer to the claimed invention (taken in its entirety) has been knowingly withheld.

In accordance with 37 C.F.R. §§ 1.97 and 1.98, a copy of each of the listed non-patent references or relevant portion thereof is also enclosed.

In accordance with 37 C.F.R. § 1.98(c), all English translations known by the undersigned to be within the possession, custody, control or availability of anyone designated in 37 C.F.R. § 1.56(c) of each non-English reference, if any, are also enclosed.

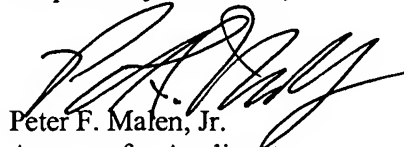
Since all listed references are either in the English language or are accompanied by a translation into English or an English language Abstract, no concise explanation of relevance is required under 37 C.F.R. § 1.98(a)(3).

Submission Fee
Under 37 C.F.R. § 1.97(c)

In accordance with 37 C.F.R. § 1.97(c), payment in the amount of \$180.00, to cover the submission fee, is enclosed to secure consideration of the references submitted with this Information Disclosure Statement. Please credit any over payment or charge any additional fees to Deposit Account No. 23-3178 of the undersigned.

Dated this 15th day of September, 2006.

Respectfully submitted,



Peter F. Malen, Jr.
Attorney for Applicants
Registration No. 45,576
Customer No. 022913
Telephone No. 801-533-9800

CERTIFICATE OF MAILING BY "EXPRESS MAIL" (37 CFR 1.10)

Applicant(s): Hofmeister et al.

Docket No.

15436.253.66.1

Application No.

10/695,342

Filing Date

October 28, 2003

Examiner

Dung T. Nguyen

Customer No.

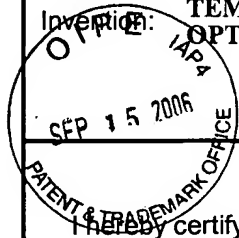
022913

Group Art Unit

2828

Invention:

TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT AND METHOD FOR FIBER OPTICS DEVICE



I hereby certify that the following correspondence:

Information Disclosure Statement (2 pgs); Form PTO-1449 (9 pgs); Transmittal Letter (2 pgs in duplicate); Copies of 39 references, Credit Card Payment Form; and postcard

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is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

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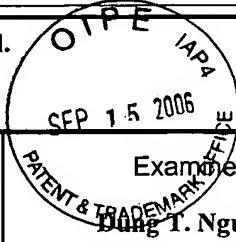
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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
15436.253.66.1

In Re Application Of: Hofmeister et al.



Application No.

Filing Date

Examiner

Customer No.

Group Art Unit

Confirmation No.

10/695,342

October 28, 2003

Dung T. Nguyen

022913

2828

5604

Title: **TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT AND METHOD FOR FIBER OPTICS DEVICE**

Address to:
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37 CFR 1.97(b)

1. ☐ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☒ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

OR

☒ the fee set forth in 37 CFR 1.17(p).

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
15436.253.66.1

In Re Application of: Hofmeister et al.

Application No.

10/695,342

Filing Date

October 28, 2003

Examiner

Dung T. Nguyen

Customer No.

022913

Group Art Unit

2828

Confirmation No.

5604

Title: **TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT AND METHOD FOR FIBER OPTICS DEVICE**

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Peter F. Malen, Jr.
Attorney for Applicants
Reg.No. 45,576
Telephone No. 801-533-9800

Dated: September 15, 2006

cc:

Applicant: Hofmeister, et al.

Serial No.: 10/695,342

Att'y Docket No.: 15436.253.66.1

Filing Date: October 28, 2003

Group: 2828

For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICEINFORMATION DISCLOSURE CITATIONS MADE BY APPLICANTU.S. Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>
____ 1	4,359,553	11/16/1982	Edwards
____ 2	4,378,451	03/29/1983	Edwards
____ 3	4,687,924	08/18/1987	Galvin et al.
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____ 5	4,747,091	05/24/1988	Doi
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____ 10	5,039,194	08/13/1991	Block et al.
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____ 15	5,383,208	01/17/1995	Queniat et al.

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Applicant: Hofmeister, et al.

Serial No.: 10/695,342

Att'y Docket No.: 15436.253.66.1

Filing Date: October 28, 2003

Group: 2828

For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE

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For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE

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For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE

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Date Considered:

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For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE

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For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
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Other Documents

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Examiner

Initial*

_____ 107	Yi Cai et al., "Jitter testing for gigabit serial communication transceivers," Jan – Feb 2002, IEEE Design and Test of Computers, Vol. 19, Issue 1, pp 66-74.
_____ 108	MAEDA, Noriyuki "Notification of Reason(s) for Refusal," Japanese Patent Application No. JP2002-563630, Nakamura, M. et al., July 13, 2005.
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For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE

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- _____ 114 Webopedia.com: 12C [online] [retrieved 11/11/03]. Retrieved from Internet: URL: <http://www.webopedia.com/TERM/I/12C.html>
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- _____ 116 Documentation entitled "IR Receiver ASSP: T2525", copyright 2003 by Atmel Corporation
- _____ 117 Documentation entitled "IR Receiver for Data Communication: U2538B", copyright 2003 by Atmel Corporation
- _____ 118 Documentation entitled "Low-Voltage Highly Selective IR Receiver IC: T2527", copyright 2002 by Atmel Corporation
- _____ 119 Documentation entitled "Application Note: T2525/26/27", copyright 2003 by Atmel Corporation
- _____ 120 *LXT16706/16707 SerDes Chipset*, Intel Products, www.intel.com/design/network/products/optical/phys/1xt16706.htm, April 19, 2002.
- _____ 121 *LXT35401 XAUI-to-Quad 3.2G Transceiver*, Intel Products, www.intel.com/design/network/products/optical/phys/1xt35401.htm, April 19, 2002
- _____ 122 Texas Instruments User's Guide, *TLK2201 Serdes EVM Kit Setup and Usage*, Mixed Signal DSP Solutions, July 2000.
- _____ 123 Texas Instruments User's Guide, *TLK1501 Serdes EVM Kit Setup and Usage*, Mixed Signal Products, June 2000.

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Date Considered:

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Group: 2828

For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE124 National Semiconductor DS92LV16 Design Guide, *Serializing Made Simple*, February 2002.125 Vaishali Semiconductor, *Fibre Channel Transceiver*, VN16117, MDSN-0002-02, 08/09/2001.126 Fairchild Semiconductor, Application Note 77, *CMOS, the Ideal Logic Family*, January 1983.

127 Analog Target Specification, Annex 48B, Published by IEEE New York, 05/2001, pp. 6-14.

References Cited by Applicants

While the filing of Information Disclosure Statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper Information Disclosure Statement, Form PTO-1449 shall be accompanied by a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), an explanation of relevance of each reference not in the English language, and should be submitted in a timely manner as set out in MPEP Sec. 609.

Examiners will consider all citations submitted in conformance with 37 C.F.R. § 1.98 and MPEP Sec. 609 and place their initials adjacent the citations in the spaces provided on this form. Examiners will also initial citations not in conformance with the guidelines which may have been considered. A reference may be considered by the Examiner for any reason whether or not the citation is in full conformance with the guidelines. A line will be drawn through a citation if it is not in conformance with the guidelines AND has not been considered. A copy of the submitted form, as reviewed by the Examiner, will be returned to the applicant with the next communication. The original of the form will be entered into the application file.

Each citation initialed by the Examiner will be printed on the issued patent in the same manner as references cited by the Examiner on Form PTO-892.

The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

GPM0000002495V001

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